

SOT-23 Plastic-Encapsulate Transistors

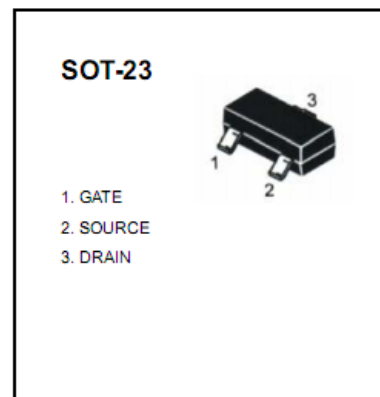
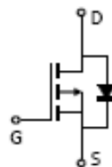
3401 MOSFET(P-Channel)

FEATURES

PWM applications

Load switch

Power management



MAXIMUM RATINGS (TA=25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V _{DS}	Drain-Source voltage	-12	V
V _{GS}	Gate-Source voltage	±12	V
I _D	Drain current	-3	A
P _D	Power Dissipation	1	W
T _j	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55-150	°C

ELECTRICAL CHARACTERISTICS (T_{amb}=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	-12			V
Gate-Threshold Voltage	Vth(GS)	VDS= VGS, ID=-250 uA	-0.4	-0.7	-1	V
Gate-body Leakage	IGSS	VDS=0V, VGS= ± 12V			± 100	nA
Zero Gate Voltage Drain Current	IBSS	VDS=-12V, VGS=0V			-1	uA
Drain-Source On-Resistance	rDS(ON)	VGS=-4. 5V, ID=-2.5A		42	60	m Ω
		VGS=-2. 5V, ID=-2.0A		47	100	m Ω
Forward Trans conductance	gfs	VDS=-5V, ID=-1.0A	6			s
Dynamic Characteristics						
Input Capacitance	Ciss	VDS=-10V, VGS=0V, f=1MHz		405		pF
Output Capacitance	Coss			112		
Reverse Transfer Capacitance	Crss			89		
Switching Capacitance						
Turn-on Delay Time	td(on)	VDD=-10V, RL=2.9Ω , VGS=-4. 5V RGEN=10 Ω		11		nS
Turn-on Rise Time	tr			35		nS
Turn-off Delay Time	td(off)			30		nS
Turn-off Fall Time	tr			10		nS
Total Gate Charge	Qg	VDS=-10V, ID=-2. 5A, VGS=-4. 5V,		9.0		nC
Gate-Source Charge	Qgs			1.0		nC
Gate-Drain Charge	Qgd			2.5		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	VSD	VGS=0V, ID=-3A			-1.2	V
Diode Forward Current	IS				-3.0	A

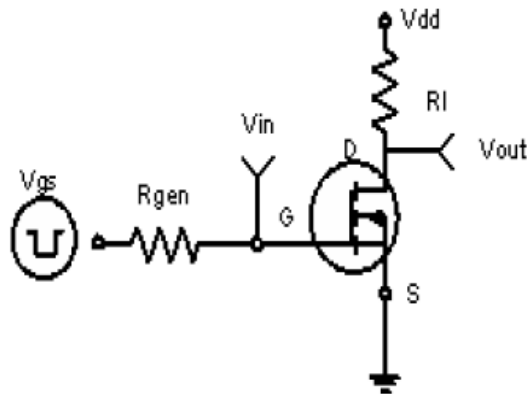


Figure 1: Switching Test Circuit

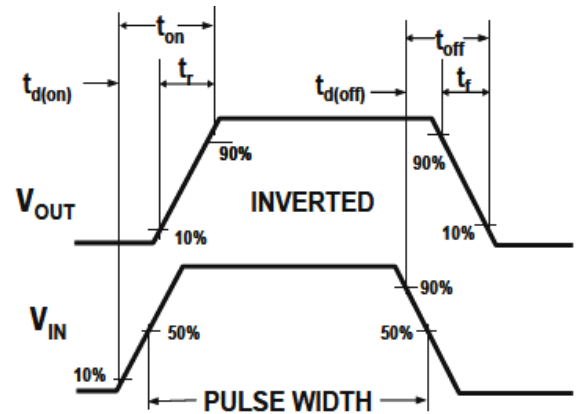


Figure 2: Switching Waveforms

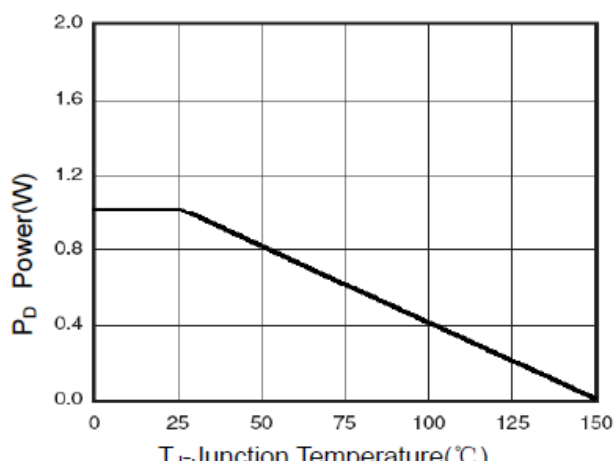


Figure 3 Power Dissipation

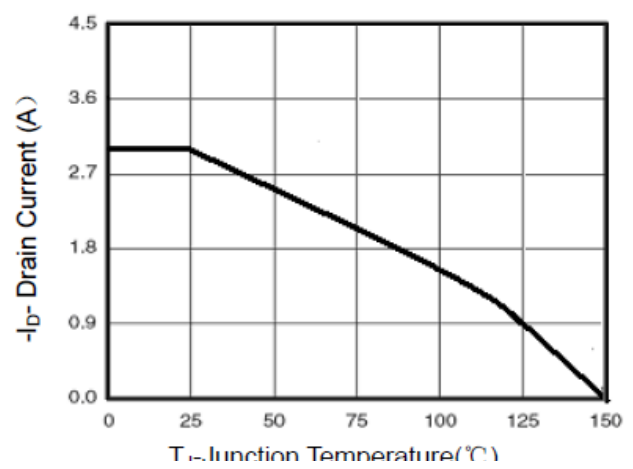


Figure 4 Drain Current

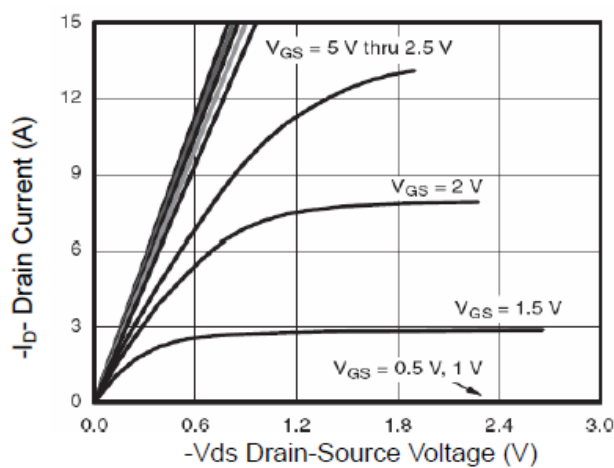


Figure 5 Output Characteristics

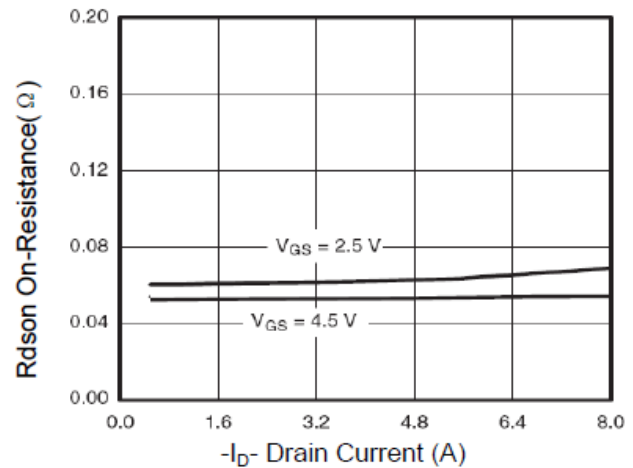


Figure 6 Drain-Source On-Resistance

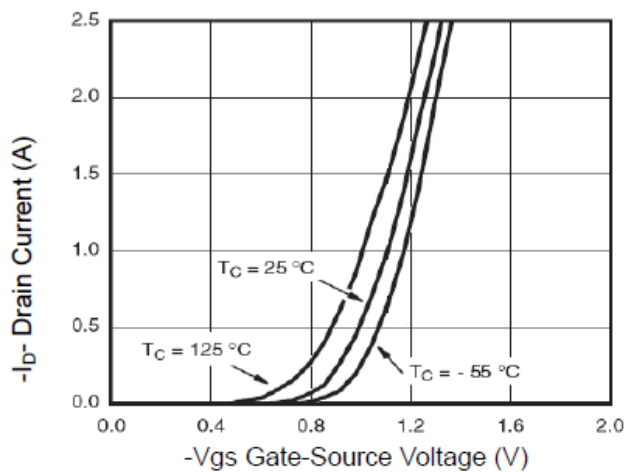


Figure 7 Transfer Characteristics

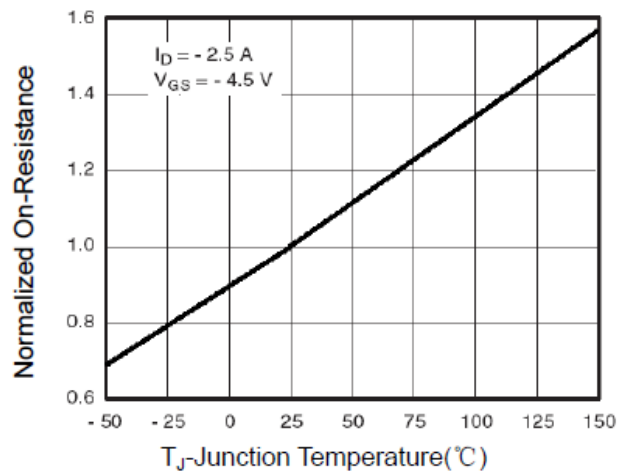


Figure 8 Drain-Source On-Resistance

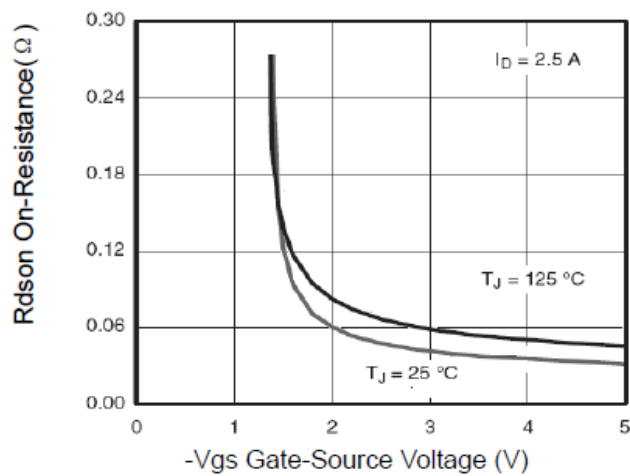


Figure 9 Rdson vs Vgs

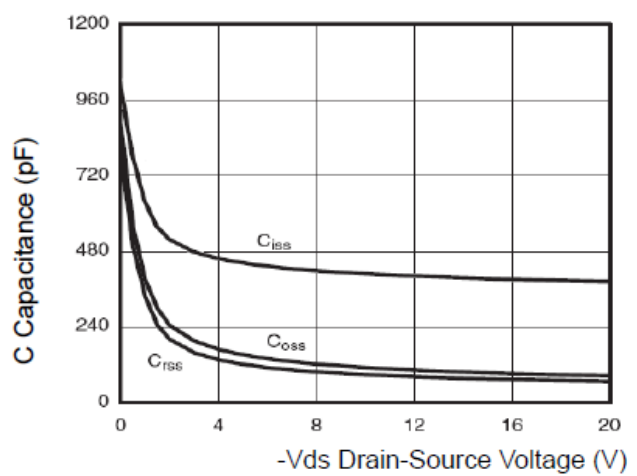


Figure 10 Capacitance vs Vds

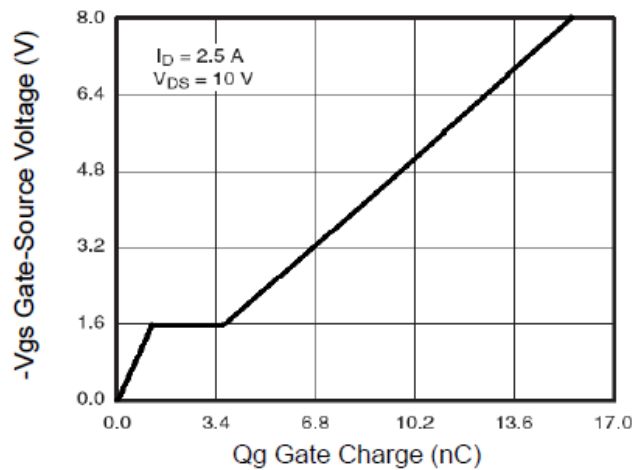


Figure 11 Gate Charge

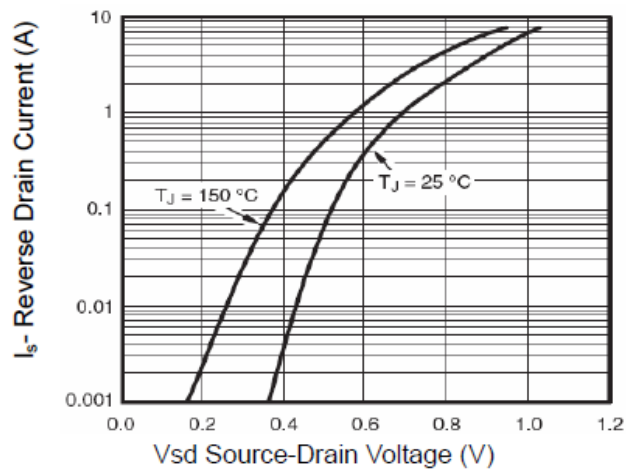


Figure 12 Source- Drain Diode Forward